

DESCRIPTION

The AL2G32 dual 2-input positive-OR gate is designed for 1.65V to 5.5V Vcc operation

The AL2G32 performs the Boolean function Y=A+B $\overline{-}$ or Y= \overline{A} $\bullet \overline{B}$ in positive logic.

The AL2G32 is fully specified for partial-powerdown applications using loff. The loff circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The AL2G32 is available in MSOP8 package.

ORDERING INFORMATION

| Package Type | Part Number | | | |
|--------------------------------|---|-------------|--|--|
| MSOP8 | | AL2G32MS8R | | |
| SPQ: 4,000pcs/Reel | MS8 | AL2G32MS8VR | | |
| Note | V: Halogen free Package R: Tape & Reel | | | |
| AiT provides all RoHS products | | | | |

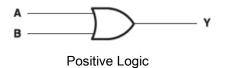
FEATURES

- Supports 5V Vcc Operation
- Inputs Accept Voltages to 5.5V
- Operating Voltage Range:1.65V to 5.5V
- Low Power Consumption:1µA (Max)
- High Output Drive: ±24mA at V_{CC}=3.0V
- Ioff Supports Partial -Power-Down Mode Operation
- Operating Temperature: -40°C to +125°C

APPLICATION

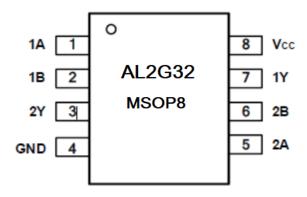
- Down Translation
- Logical OR
- AV Receiver
- Blu-Ray Player ad Home Theater
- Digital Photo Frame (DPF)
- High-Speed Data Acquisition and Generation
- Personal Navigation Device (GPS)
- Portable Media Player

FUNCTIONAL BLOCK DIAGRAM



REV1.0 - OCT 2020 RELEASED -

PIN DESCRIPTION



MSOP8, MS8

Top View

| Pin # | Comple of | 1/0 | Function | |
|-------|-----------|-----|-----------------------|--|
| MSOP8 | Symbol | I/O | Function | |
| 1 | 1A | I | Channel 1 Logic Input | |
| 2 | 1B | | Channel 2 Logic Input | |
| 3 | 2Y | 0 | Logic Level Output 2 | |
| 4 | GND | - | Ground | |
| 5 | 2A | I | Channel 2 Logic Input | |
| 6 | 2B | | Channel 2 Logic Input | |
| 7 | 1Y | 0 | Logic Level Output 1 | |
| 8 | Vcc | - | Power Supply | |

FUNCTION TABLE

| INI | OUTPUT | |
|-----|--------|---|
| Α | В | Υ |
| Н | Н | Н |
| L | Н | Н |
| Н | L | Н |
| L | L | L |

Y=A+B

H=Hight Voltage Level

L=Low Voltage Level

ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range, unless otherwise noted (1)(2)

| over operating nee-all temperature range | s, urness ourcewise ne | neu () (| | | |
|--|---|-----------|----------------------------|--|--|
| V _{CC} , Supply Voltage Range | | | -0.5V ~ 6.5V | | |
| V _I , Input Voltage Range (2) | | | -0.5V ~ 6.5V | | |
| Vo, Voltage range applied to any output in the high-impedance or power-off state (2) | | | -0.5V ~ 6.5V | | |
| Vo, Voltage range applied to any output | in the high or low sta | te (2)(3) | $-0.5V \sim V_{CC} + 0.5V$ | | |
| Ік, Input Clamp Current | I _{IK} , Input Clamp Current V _I <0 | | | | |
| Iок, Output Clamp Current Vo<0 | | | -50mA | | |
| Io, Continuous Output Current | | | ±50mA | | |
| Continuous Current Through V _{CC} or GND | | | ±100mA | | |
| T _J , Junction Temperature | | | 150°C | | |
| T _{STG} , Storage Temperature | | | -65°C ~ 150°C | | |
| ESD Ratings | | | | | |
| V Electrostatic Discharge | Human-body Model (HBM) | | ±8000V | | |
| V _(ESD) , Electrostatic Discharge | Machine Model (MM) | | ±500V | | |

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

RECOMMENDED OPERATING CONDITIONS

over recommended operating free-air temperature range, T_A = +25°C, unless otherwise noted.

| Parameter | Symbol | Conditions | Min. | Max. | Unit | |
|-------------------------------|---------------------------------|---------------------------|----------|----------|------|--|
| Cumply Voltage | \/ | Operating | 1.65 | 5.5 | V | |
| Supply Voltage | V _{CC} | Data retention only | 1.5 | 5.5 | V | |
| | | Vcc=1.65V to 1.95V | 0.65xVcc | | | |
| l liab I aval lanvit Valtaga | \ / | Vcc=2.3V to 2.7V | 1.7 | | V | |
| High-Level Input Voltage | V _{IH} | Vcc=3V to 3.6V | 2.2 | | \ \ | |
| | | Vcc=4.5V to 5.5V | 0.7xVcc | | | |
| | VIL | Vcc=1.65V to 1.95V | | 0.15xVcc | V | |
| Lave Lave Heavet Maltana | | Vcc=2.3V to 2.7V | | 0.3 | | |
| Low-Level Input Voltage | | Vcc=3V to 3.6V | | 0.4 | | |
| | | Vcc=4.5V to 5.5V | | 0.15xVcc | | |
| Input Voltage | Vı | | 0 | 5.5 | V | |
| Output voltage | Vo | | 0 | Vcc | V | |
| Input Transition Rise or Fall | t _r , t _f | Vcc=1.8V±0.15V, 2.5V±0.2V | | 20 | | |
| | | Vcc=3.3V±0.3V | | 10 | ns/V | |
| | | Vcc=5V±0.5V | | 5 | | |
| Operating temperature | T _A | | -40 | 125 | °C | |

REV1.0 - OCT 2020 RELEASED - - 3 -

⁽³⁾ The value of V_{CC} is provided in the Recommended Operating Conditions table.

ELECTRICAL CHARACTERISTICS

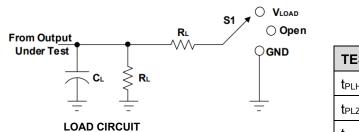
 $T_A = +25$ °C, unless otherwise noted

| Parameter | | Conditions Temp | | Min. | Тур. | Max. | Unit | |
|-----------------------------|---|---|--------------|-------------------|------|------|----------|--|
| DC C | haracteristics | | | • | • | • | | |
| | | I _{OH} = -100μA, V _{CC} =1.65V to 5.5V | | V _{CC} - | - | - | | |
| | | I _{OH} =-4mA, V _{CC} =1.65V | | 1.2 | - | - |] v | |
| Vон | | I _{OH} =-8mA, V _{CC} =2.3V | -40°C~+125°C | 1.9 | - | - | | |
| | | I _{OH} =-16mA, V _{CC} =3V | | 2.4 | - | - | | |
| | | I _{OH} =-24mA, V _{CC} =3V | | 2.3 | - | - | 1 | |
| | | I _{OH} =-32mA, V _{CC} =4.5V | | 3.8 | - | - | 1 | |
| | | I _{OL} =100μA, V _{CC} =1.65V to 5.5V | | - | - | 0.1 | | |
| | | I _{OL} =4mA, V _{CC} =1.65V | | - | - | 0.45 | 1 | |
| | | I _{OL} =8mA, V _{CC} =2.3V | 4000 - 40500 | - | - | 0.3 | V | |
| Vol | | I _{OL} =16mA, V _{CC} =3V | -40°C~+125°C | - | - | 0.4 | | |
| | | I _{OL} =24mA, V _{CC} =3V | | - | - | 0.55 | | |
| | | I _{OL} =32mA, V _{CC} =4.5V | | - | - | 0.55 | 1 | |
| | | V _I =5.5V or GND, | +25°C | - | ±0.1 | ±1 | μA | |
| I _I A or B input | | V _{CC} = 0V to 5.5V -40°C~+125°C | | - | - | ±5 | | |
| loff | | V _I or V _O =5.5V, | +25°C | | ±0.1 | ±1 | | |
| | | Vcc=0V -40°C~+125°C | | | | ±10 | μA | |
| | | V _I =5.5V or GND, I _O =0, | +25°C | - | 0.1 | 1 | | |
| Icc | | V _{CC} =1.65V to 5.5V | -40°C~+125°C | - | - | 10 | μA | |
| | | One input at V _{CC} - 0.6V, | | | | | | |
| ΔΙα | : | Other inputs at V _{CC} or GND -40°C~+125°C | | - | _ | 500 | μΑ | |
| | | V _{CC} =3V to 5.5V | | | | | | |
| AC C | haracteristics | | | | | | | |
| | | V _{CC} =1.8V±0.15V,C _L =30pF, R _L =1kΩ | | | 8.0 | | | |
| | | V _{CC} =2.5V±0.2V,C _L =30pF, R _L =500Ω | -40°C~+125°C | | 3.7 | | 1 | |
| $t_{\sf pd}$ | t _{pd} Propagation Delay | v _{cc} =3.3V±0.3V,C _L =50pF, R _L =500Ω | | | 2.5 | | ns | |
| | V _{CC} =5V±0.5V,C _L =50pF, R _L =500Ω | | | 2.7 | | | | |
| | | V _{CC} =3.3V, V _I =V _{CC} or GND | -40°C~+125°C | | 4 | | pF | |
| | P P | V _{CC} =1.8V, f=10MHz | | | 20 | | + 21 | |
| C_{pd} | Power Dissipation | V _{CC} =2.5V, f=10MHz | +25°C | | 21 | | 1 _ | |
| Capacitance | | | | | 22 | | pF | |
| | | | | | | | ┥ | |

All unused inputs of the device must be held at $\mbox{\em V}\mbox{\em co}$ or GND to ensure proper device operation.

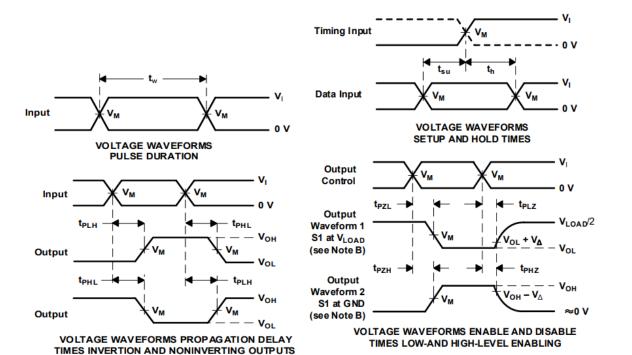
REV1.0 - OCT 2020 RELEASED - -4

PARAMETER MEASUREMENT INFORMATION



| TEST | S1 |
|------------------------------------|-------|
| t _{PLH} /t _{PHL} | open |
| tplz/tpzl | VLOAD |
| t _{PHZ} /t _{PZH} | GND |

| Vcc | INPI Vi | UTS t _r /t _f | V _M | VLOAD | C | CL | RL | VΔ |
|------------|-----------------|---------------------------------------|--------------------|-------------------------|------|------|-----|-------|
| 1.8V±0.15V | V_{CC} | ≤2ns | V _{CC} /2 | $2 \ x \ V_{\text{CC}}$ | 15pF | 30pF | 1ΜΩ | 0.15V |
| 2.5V±0.2V | Vcc | ≤2ns | Vcc/2 | 2 x Vcc | 15pF | 30pF | 1ΜΩ | 0.15V |
| 3.3V±0.3V | 3V | ≤2.5ns | 1.5V | 6V | 15pF | 50pF | 1ΜΩ | 0.3V |
| 5.0V±0.5V | Vcc | ≤2.5ns | Vcc/2 | 2 x Vcc | 15pF | 50pF | 1ΜΩ | 0.3V |



NOTE A: C_L includes probe and jig capacitance.

- B: Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- C: All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_0 = 50\Omega$.
- D: The outputs are measured one at a time, with one transition per measurement.
- E: t_{PLZ} and t_{PHZ} are the same as t_{dis}.
- F: t_{PZL} and t_{PZH} are the same as t_{en} .
- G: t_{PLH} and t_{PHL} are the same as t_{pd} .
- H: All parameters and waveforms are not applicable to all devices.

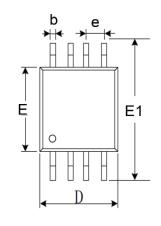
Figure 1. Load Circuit and Voltage Waveforms

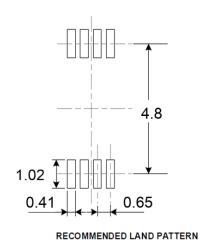
REV1.0 - OCT 2020 RELEASED - - 5 -

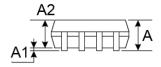


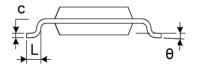
PACKAGE INFORMATION

Dimension in MSOP8 (Unit: mm)









| Symbol | Millimeters | | | | |
|--------|-------------|-------|--|--|--|
| Symbol | Min | Max | | | |
| Α | 0.820 | 1.100 | | | |
| A1 | 0.020 | 0.150 | | | |
| A2 | 0.750 | 0.950 | | | |
| b | 0.250 | 0.380 | | | |
| С | 0.090 | 0.230 | | | |
| D | 2.900 | 3.100 | | | |
| е | 0.650 BSC | | | | |
| Е | 2.900 | 3.100 | | | |
| E1 | 4.750 | 5.050 | | | |
| L | 0.400 | 0.800 | | | |
| θ | 0° | 6° | | | |

REV1.0 - OCT 2020 RELEASED - - 6 -

IMPORTANT NOTICE

AiT Semiconductor Inc. (AiT) reserves the right to make changes to any its product, specifications, to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

AiT Semiconductor Inc.'s integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life support applications, devices or systems or other critical applications. Use of AiT products in such applications is understood to be fully at the risk of the customer. As used herein may involve potential risks of death, personal injury, or server property, or environmental damage. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

AiT Semiconductor Inc. assumes to no liability to customer product design or application support. AiT warrants the performance of its products of the specifications applicable at the time of sale.

REV1.0 - OCT 2020 RELEASED - - 7 -