

**DESCRIPTION**

The AM06P04 is available in SOP-8 Package.

BVDSS	RDS(on)	ID
-40V	26 mΩ	-6.3A

APPLICATION

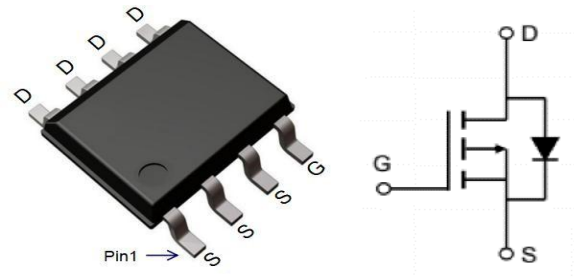
- Load Switch
- PWM Application
- Power Management

ORDERING INFORMATION

Package Type	Part Number	
SOP8 SPQ: 4,000/Reel	M8	AM06P04M8VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products		

FEATURES

- -40V, -6.3A
- RDS(ON) Typ = 26mΩ @ VGS = -10V
- RDS(ON) Typ = 37mΩ @ VGS = -4.5V
- Advanced Trench Technology
- Excellent RDS(ON) and Low Gate Charge

PIN DESCRIPTION

Pin#	Symbol	Function
1,2,3	S	Source
4	G	Gate
5,6,7,8	D	Drain

ABSOLUTE MAXIMUM RATINGS

T_J = 25°C unless otherwise specified

V _{DS} , Drain-to-Source Voltage		-40V
V _{GS} , Gate-to-Source Voltage		±20V
I _D , Continuous Drain Current	T _A = 25°C	-6.3A
	T _A = 100°C	-3.78A
I _{DM} , Pulsed Drain Current ⁽¹⁾		-25.2A
E _{AS} , Single Pulse Avalanche Energy ⁽²⁾		30mJ
P _D , Power Dissipation	T _A = 25°C	2.3W
R _{θJC} , Thermal Resistance, Junction to Case		55°C/W
T _J , T _{STG} , Junction & Storage Temperature Range		-55°C ~ +150°C

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

(1) Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

(2) E_{AS} condition Starting T_J=25°C, V_{DD}=-20V, V_G=-10V, R_G=25Ω, L=0.5mH, I_{AS}=-11A

**ELECTRICAL CHARACTERISTICS**T_J=25°C, unless otherwise noted.

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V _{(BV)DSS}	V _{GS} =0V, I _D =250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS (th)}	V _{GS} =V _{DS} , I _D =-250μA	-1.1	-1.6	-2.2	V
Static Drain Source ON-Resistance ⁽³⁾	R _{DS(ON)}	V _{GS} =-10V, I _D = -5A	-	26	34	mΩ
Static Drain Source ON-Resistance ⁽³⁾	R _{DS(ON)}	V _{GS} =-4.5V, I _D = -3A		37	48	mΩ
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = -20V, V _{GS} =0V, f=1MHz	-	887	-	pF
Output Capacitance	C _{oss}		-	92	-	
Reverse Transfer Capacitance	C _{rss}		-	79	-	
Total Gate Charge	Q _g	V _{GS} =0 to -10V, V _{DS} =-20V, I _D =-3A	-	35	-	nC
Gate Source Charge	Q _{gs}		-	35	-	
Gate-Drain Charge	Q _{gd}		-	7	-	
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	V _{GS} =-10V, V _{DD} =-20V R _{GEN} =3Ω, I _D =-5A	-	13	-	ns
Turn-On Rese Time	t _r		-	10	-	
Turn-Off Delay Time	t _{d(off)}		-	20	-	
Turn-Off Fall Time	t _f		-	12	-	
Drain-Source Diode Characteristics and Max Ratings						
Maximum Continuous Drain to Source Diode Forward Current	I _S	-	-	-	-6.3	A
Maximum Pulsed Drain to Source Diode Forward Current	I _{SM}	-	-	-	-25.2	A
Drain to Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-5A	-	-	-1.2	V
Body Diode Reverse Recovery Time	T _{rr}	I _F =-3A, di/dt =100A/us	-	23	-	nS
Body Diode Reverse Recovery Charge	Q _{rr}		-	15	-	nC

(3) R_{θJA} is measured with the device mounted on a 1inch2 pad of 2oz copper FR4 PCB

(4) Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.



TEST CIRCUIT

Fig 1. Gate Charge Test Circuit & Waveform

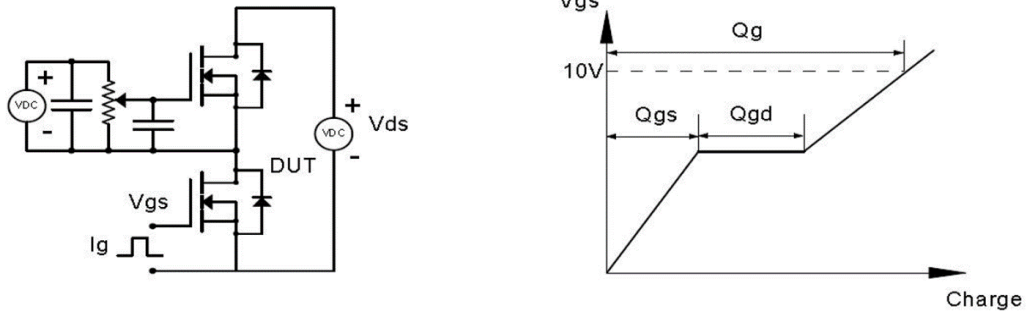


Fig 2. Resistive Switching Test Circuit & Waveform

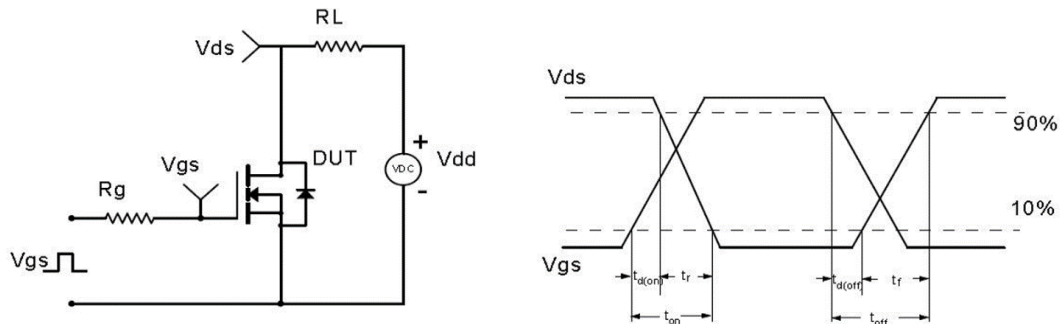


Fig 3. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

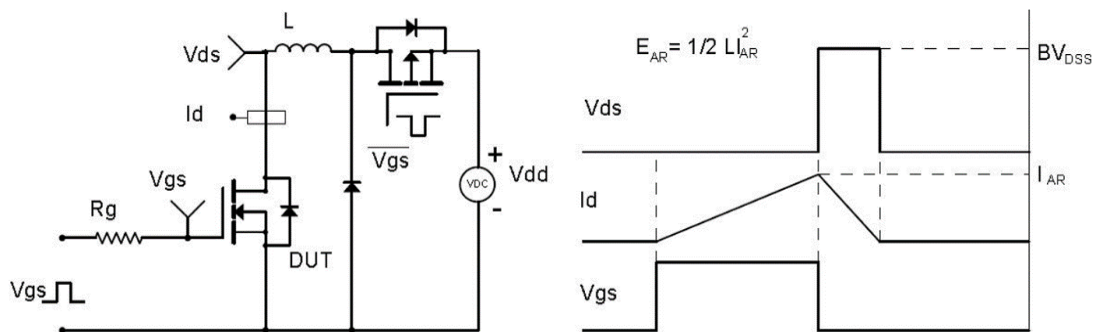
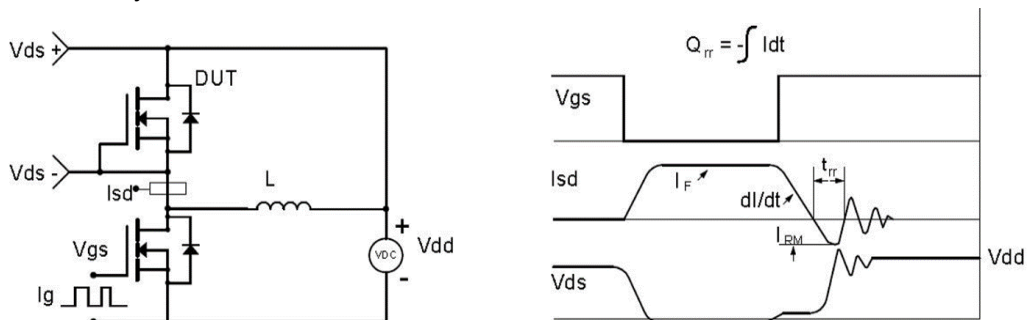


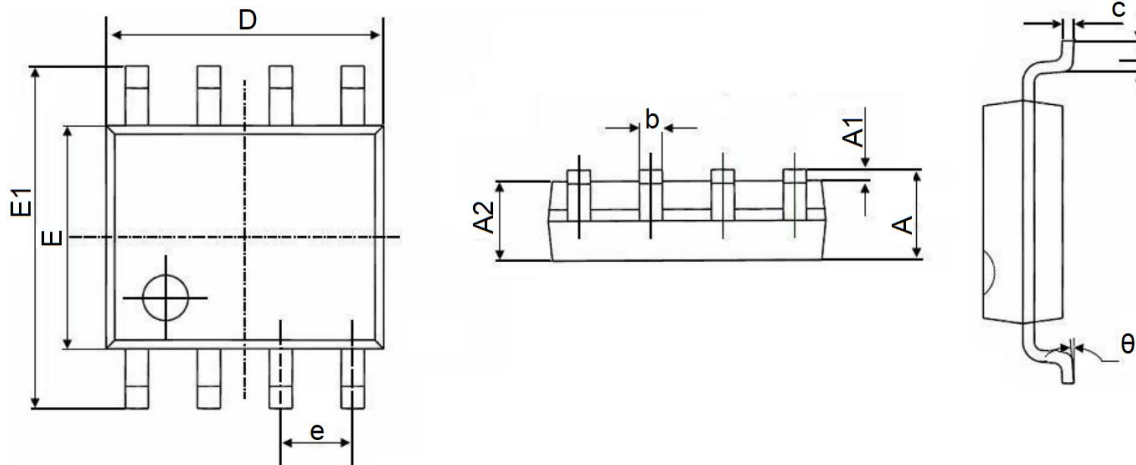
Fig 4. Diode Recovery Test Circuit & Waveform





PACKAGE INFORMATION

Dimension in SOP-8 (Unit: mm)



Symbol	Millimeters	
	Min.	Max.
A	1.350	1.750
A1	0.100	0.250
A2	1.350	1.550
b	0.330	0.510
c	0.170	0.250
D	4.700	5.100
E	3.800	4.000
E1	5.800	6.200
e	1.270 BSC	
L	0.400	1.270
θ	0°	8°



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