

DESCRIPTION

This monolithic integrated circuit is an adjustable

3-terminal positive voltage regulator designed to
supply more than 1A of load current with an output
voltage adjustable over a 1.2 to 37V. It employs
internal current limiting, thermal shut-down and safe
area compensation.

The A317 is available in SOT-223 package.

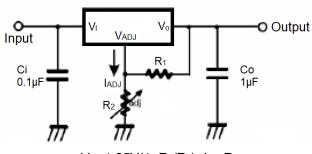
FEATURES

- Internal thermal overload protection
- Internal short circuit current limiting
- Output transistor safe operating area compensation
- Available in SOT-223 package

ORDERING INFORMATION

Package Type	Part Number			
SOT-223	N.I	A317NR		
SPQ: 2,500pcs/Reel	N	A317NVR		
NI-4-	V: Halogen free Package			
Note	R: Tape & Reel			
AiT provides all RoHS products				

TYPICAL APPLICATION



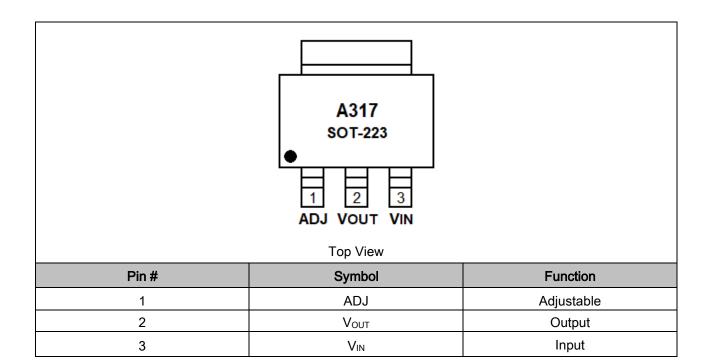
 $V_0=1.25V(1+R_2/R_1)+I_{ADJ}R_2$

 C_{I} is required when regulator is located an appreciable distance from power supply filter.

 $\ensuremath{\text{C}}_{\text{O}}$ is not needed for stability, however, it does improve transient response.

Since I_{ADJ} is controlled to less than $100\mu A$, the error associated with this term is negligible in most applications.

PIN DESCRIPTION



ABSOLUTE MAXIMUM RATINGS

V _{IN} -V _{OUT} , Input - Output Voltage Difference	40V
T _{LEAD} , Lead Temperature	230°C
P _D , Power Dissipation	Internal limited
T _J , Operating Junction Temperature Range	0°C~+150°C
T _{STG} , Storage Temperature Range	-55°C~150°C
ΔV _O /ΔT, Temperature Coefficient of Output Voltage	±0.02%/°C

Stresses above may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

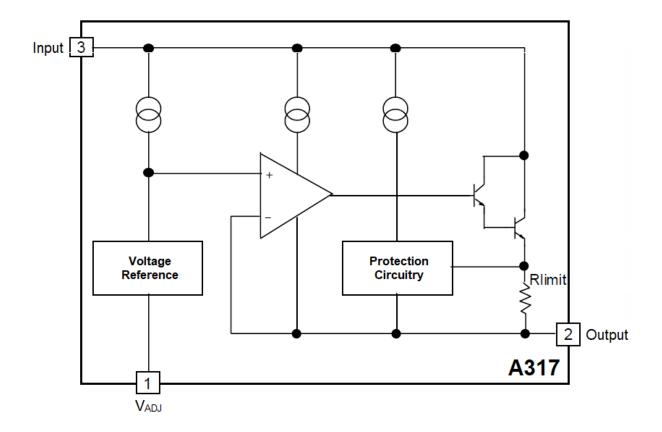
 V_0 - V_1 =5V, I_0 =0.5A, $0^{\circ}C \le T_J \le +125^{\circ}C$, I_{MAX} =1.5A, P_{MAX} =20W,unless otherwise specified

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Line Regulation ^{NOTE1}	R _{LINE}	3V≤V _I -V _O ≤40V, T _A =25°C		ı	0.01	0.04	%/V
Line Regulation (1972)		3V≤V _I -V _O ≤40V		ı	0.02	0.07	
Load Regulation ^{NOTE1}	RLOAD	10mA≤I _O ≤I _{MAX} ,	Vo<5V	18		25	
		T _A =25°C	V _O ≥5V	1	0.4	0.5	mV%/
		10m 1 < 1 < 1	Vo<5V	- 40 0.8	70	Vo	
		10mA≤I ₀ ≤I _{MAX}	Vo≥5V		0.8	1.5	
Adjustable Pin Current	l _{ADJ}	·		ı	46	100	μΑ
Adjustable Pin Current	Δl _{ADJ}	$3V \le V_I - V_O \le 40V$ $0mA \le I_O \le I_{MAX}, P_D \le P_{MAX}$		-	2.0	5.0	μΑ
Change	ΔIADJ						
Reference Voltage	V _{REF}	3V≤V _{IN} -V _O ≤40V		1.20	1.25	1.30	V
		10mA≤Io≤I _{MAX} , P _D ≤P _{MAX}					
Temperature Stability	ST⊤			-	0.7	1	%/Vo
Minimum Load Current to	Lams	I _{L(MIN)} V _{IN} -V _{OUT} = 40V			3.5	12	mA
Maintain Regulation	IL(MIN)			- 3.5		12	IIIA
Maximum Output Current	Io(MAX)	V_I - V_O \leq 15 V , P_D \leq P_{MAX}		1.0	2.2	- A	
Maximum Output Current		V _I -V _O ≤40V, P _D ≤P _{MAX} , T _A =25°C			0.3	1	٨
RMS Noise,% of Vout	en	T _A =25°C, 10Hz≤f≤10kHz		-	0.003	0.01	%/Vo
Ripple Rejection	RR	V _O =10V, f =120Hz					
		without CADJ			60	-	dB
		C _{ADJ} =10µF ^{NOTE2}		66	75		
Long-Term Stability,	ST	T _A =25°C for end point mesasurements,1000HR		-	0.3	1	%
T _J =T _{HIGH}	31						
Thermal Resistance	R _{eJC}			-	5	-	°C/W
Junction to case	I V AJC				J	-	C/VV

NOTE1: Load and line regulation are specified at constant junction temperature. Change in V_D due to heating effects must be taken into account separately. Pulse testing with low duty is used.(P_{MAX} =20W)

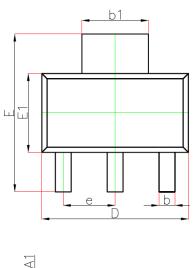
NOTE2: C_{ADJ}. when used, is connected between the adjustment pin and ground.

BLOCK DIAGRAM

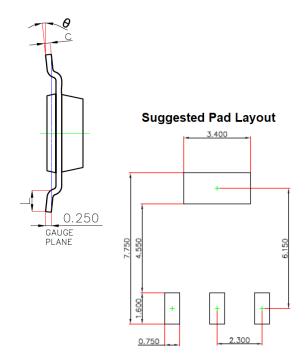


PACKAGE INFORMATION

Dimension in SOT-223 (Unit: mm)







Comphal	Millimeters		Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	-	1.800	-	0.071	
A1	0.020	0.100	0.001	0.004	
A2	1.500	1.700	0.059	0.067	
b	0.660	0.840	0.026	0.033	
b1	2.900	3.100	0.114	0.122	
С	0.230	0.350	0.009	0.014	
D	6.300	6.700	0.248	0.264	
E	6.700	7.300	0.264	0.287	
E1	3.300	3.700	0.130	0.146	
е	2.300 BSC		0.091 BSC		
L	0.750	ı	0.030	-	
θ	0°	10°	0°	10°	

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