Product Features: Small surface mountable, Solid state, Faster time to trip than standard SMD devices, Lower resistance than standard SMD devices
Operation Current: 0.50A~7.00A
Maximum Voltage: 6V_{DC}
Temperature Range : -40℃ to 85℃
Applications: All high-density boards

Electrical Characteristics (23℃)

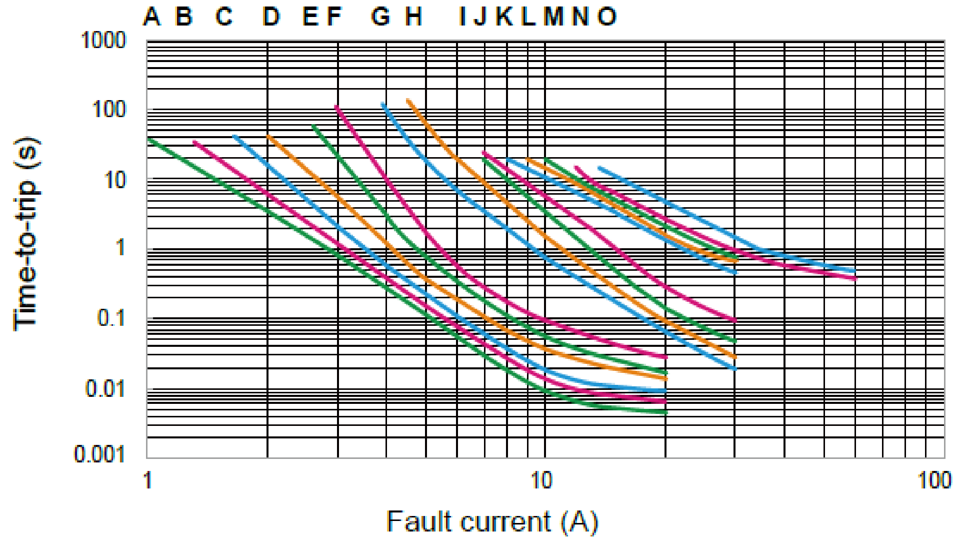
Part Number	Hold Current	Trip Current	Rated Voltage	Max Current	Typical Power	Max Time to Trip		Resistance	
						Current	Time	R_{MIN}	$R1_{MAX}$
	I_H, A	I_T, A	V_{MAX},V_{DC}	I_{MAX}, A	Pd, W	A	Sec	Ohms	Ohms
F1206L050SL-06	0.50	1.50	6	100	0.8	8.0	0.20	0.025	0.200
F1206L075SL-06	0.75	1.80	6	100	0.8	8.0	0.30	0.018	0.180
F1206L110SL-06	1.10	2.20	6	100	0.8	8.0	0.30	0.015	0.100
F1206L150SL-06	1.50	3.00	6	100	0.8	8.0	0.30	0.010	0.065
F1206L175SL-06	1.75	3.50	6	100	0.8	8.0	0.40	0.005	0.030
F1206L200SL-06	2.00	4.00	6	100	0.8	8.0	0.50	0.005	0.025
F1206L260SL-06	2.60	5.20	6	100	0.8	8.0	4.00	0.003	0.025
F1206L300SL-06	3.00	6.00	6	100	0.8	8.0	4.00	0.003	0.020
F1206L350SL-06	3.50	7.00	6	100	0.8	8.0	5.00	0.003	0.018
F1206L380SL-06	3.80	8.00	6	100	0.8	8.0	5.00	0.002	0.014
F1206L400SL-06	4.00	8.00	6	100	0.8	8.0	5.00	0.002	0.014
F1206L450SL-06	4.50	9.00	6	100	0.8	22.5	2.00	0.001	0.014
F1206L500SL-06	5.00	10.00	6	100	0.8	25.0	5.00	0.001	0.012
F1206L600SL-06	6.00	12.00	6	100	1.0	30.0	2.00	0.001	0.010
F1206L700SL-06	7.00	14.00	6	100	1.0	35.0	2.00	0.001	0.008

Thermal Derating for PPTC Device at Various Ambient Temperatures

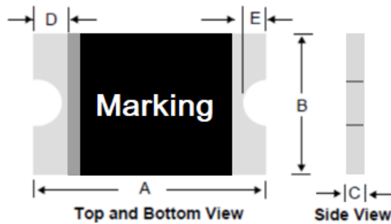
Temperatures	-40℃	-20℃	0℃	23℃	30℃	40℃	50℃	60℃	70℃	85℃
Current Derating %	145%	130%	115%	100%	92%	84%	77%	69%	61%	50%

Typical Time-To-Trip at 23℃

A = F1206L050SL-06
B = F1206L075SL-06
C = F1206L110SL-06
D = F1206L150SL-06
E = F1206L175SL-06
F = F1206L200SL-06
G = F1206L260SL-06
H = F1206L300SL-06
I = F1206L350SL-06
J = F1206L380SL-06
K = F1206L400SL-06
L = F1206L450SL-06
M = F1206L500SL-06
N = F1206L600SL-06
O = F1206L700SL-06



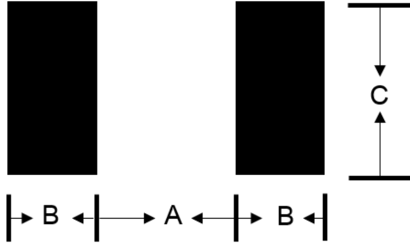
Product Dimensions (Millimeters)



Part Number	A		B		C		D		E	
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max
F1206L050SL-06	3.00	3.50	1.50	1.80	0.30	0.70	0.25	0.75	0.10	0.45
F1206L075SL-06	3.00	3.50	1.50	1.80	0.30	0.70	0.25	0.75	0.10	0.45
F1206L110SL-06	3.00	3.50	1.50	1.80	0.30	0.70	0.25	0.75	0.10	0.45
F1206L150SL-06	3.00	3.50	1.50	1.80	0.30	0.70	0.25	0.75	0.10	0.45
F1206L175SL-06	3.00	3.50	1.50	1.80	0.30	0.70	0.25	0.75	0.10	0.45
F1206L200SL-06	3.00	3.50	1.50	1.80	0.30	0.70	0.25	0.75	0.10	0.45
F1206L260SL-06	3.00	3.50	1.50	1.80	0.30	1.00	0.25	0.75	0.10	0.45
F1206L300SL-06	3.00	3.50	1.50	1.80	0.30	1.00	0.25	0.75	0.10	0.45
F1206L350SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45
F1206L380SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45
F1206L400SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45
F1206L450SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45
F1206L500SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45
F1206L600SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45
F1206L700SL-06	3.00	3.50	1.50	1.80	0.60	1.00	0.25	0.75	0.10	0.45

Pad Layouts、Solder Reflow and Rework Recommendations

The dimension in the table below provide the recommended pad layout for each F1206L device



Pad dimensions (millimeters)			
Device	A Nominal	B Nominal	C Nominal
F1206L	2.00	1.00	1.90

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (Tsmax to Tp)	3℃/second max.
Preheat: Temperature Min (Tsmin) Temperature Max Tsmax) Time (t_S Tsmin to Tsmax)	 150℃ 200℃ 60~180 seconds
Time maintained above: Temperature(T_L) Time (t_L)	 217℃ 60~150 seconds
Peak/Classification Temperature(Tp):	260℃
Time within 5℃ of actual Peak: Temperature (tp)	 20~40 seconds
Ramp-Down Rate:	6℃/second max.
Time 25℃ to Peak Temperature:	8 minutes max.

Note 1: All temperatures refer to of the package, measured on the package body surface.

Solder reflow

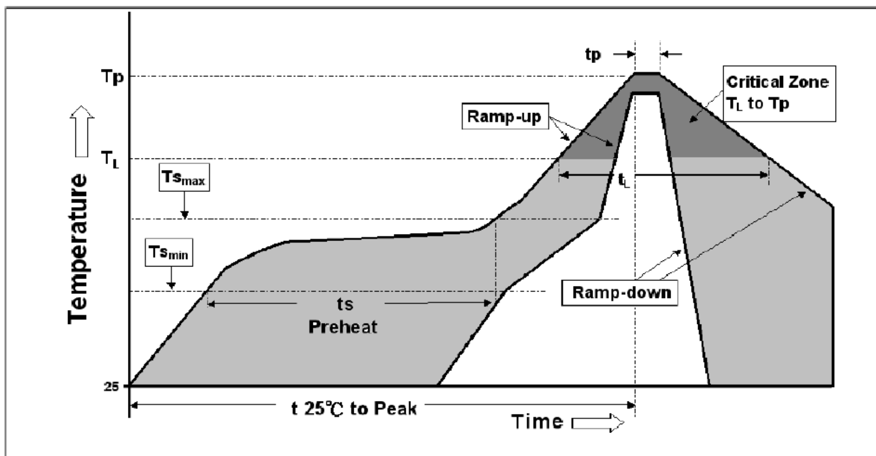
※ Due to "Lead Free" nature, Temperature and Dwelling time for the soldering zone is higher than those for Regular. This may cause damage to other components.

1. Recommended max past thickness > 0.25mm.
2. Devices can be cleaned using standard methods and aqueous solvent.
3. Rework use standard industry practices.
4. Storage Environment: < 30℃ / 60%RH

Caution:

1. If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.
2. Devices are not designed to be wave soldered to the bottom side of the board.

Reflow Profile



NOTE: Specification subject to change without notice.