



DESCRIPTION

The AM2303 is the P-Channel logic enhancement mode power field effect transistor is produced using high cell density, advanced trench technology to provide excellent $R_{DS(ON)}$ low gate charge and operation gate as 2.5V.

This device is suitable for use as a load switch or other general applications.

The AM2303 is available in SOT-23 package

ORDERING INFORMATION

Package Type	Part Number	
SOT-23	E3	AM2303E3R
		AM2303E3VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products Suffix " V " means Halogen free Package		

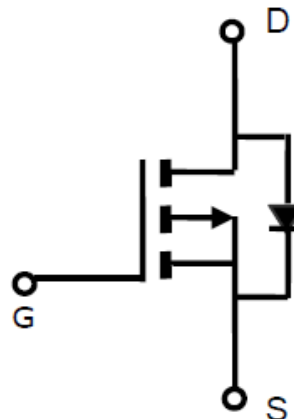
FEATURES

- -30V/-4.3A, $R_{DS(ON)} = 50m\Omega$ (typ.)@ $V_{GS} = -10V$
- -30V/-3.5A, $R_{DS(ON)} = 58m\Omega$ (typ.)@ $V_{GS} = -4.5V$
- -30V/-2.5A, $R_{DS(ON)} = 73m\Omega$ (typ.)@ $V_{GS} = -2.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and Maximum DC current capability
- Available in SOT-23 package

APPLICATIONS

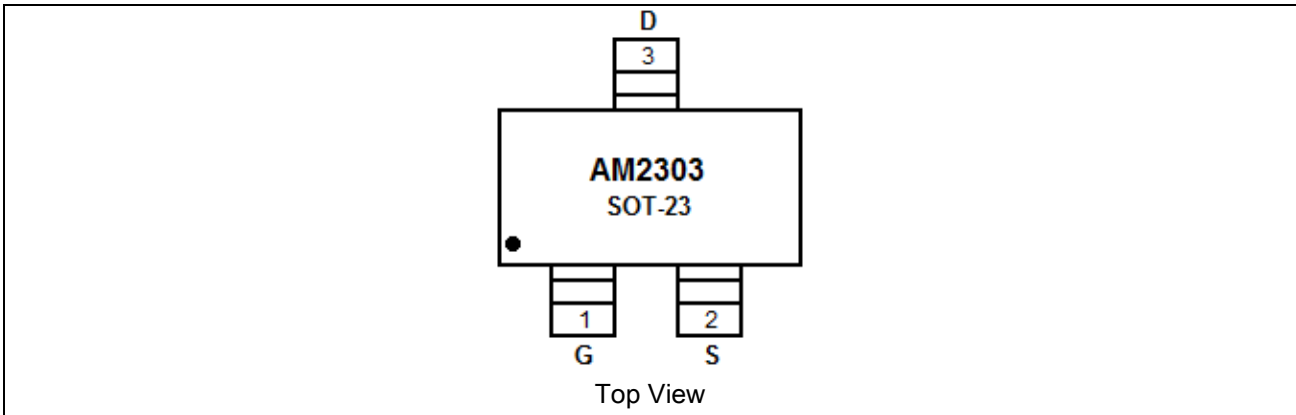
- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- DC/DC Converter
- Load Switch

P CHANNEL MOSFET





PIN DESCRIPTION



Pin #	Symbol	Function
1	G	Gate
2	S	Source
3	D	Drain



ABSOLUTE MAXIMUM RATINGS

T_A = 25°C, unless otherwise noted

V _{DSS} , Drain-Source Voltage		-30V
V _{GSS} , Gate-Source Voltage		±12V
I _D , Continuous Drain Current , V _{GS} = -10V ^{NOTE1}	T _c =25°C	-4.3A
	T _c =70°C	-3.8A
I _{DM} , Pulsed Drain Current ^{NOTE2}		-20A
P _D , Power Dissipation	T _A =25°C	1.25W
	T _A =70°C	0.8W
T _J , Operation Junction Temperature		-55°C~150°C
T _{STG} , Storage Temperature Range		-55°C~150°C

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

NOTE1: The value of R_{θJA} is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C.

NOTE2: The data tested by pulsed , pulse width≤300uS , duty cycle≤2%

THERMAL INFORMATION

Symbol	Max	Unit
R _{θJA}	120	°C/W
R _{θJL}	80	°C/W



ELECTRICAL CHARACTERISTICS

T_A = 25°C, unless otherwise noted

Parameter	Symbol	Conditions	Min	Typ.	Max	Units
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.6	-	-1.0	V
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
Zero Gate Voltage Drain-Source Leakage Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V T _J =25°C	-	-	-1	μA
		V _{DS} =-24V, V _{GS} =0V T _J =55°C	-	-	-5	
Drain-source On-Resistance ^{NOTE2}	R _{DS(ON)}	V _{GS} =-10V, I _D =-4.3A	-	50	58	mΩ
		V _{GS} =-4.5V, I _D =-3.5A	-	58	65	
		V _{GS} =-2.5V, I _D =-2.5A	-	73	92	
Forward Transconductance	G _{FS}	V _{DS} =-5V, I _D =-4.0A	-	10	-	S
Source-Drain Diode						
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.7	-1.0	V
Continuous Source Current ^{NOTE1,3}	I _S		-	-	-5.6	A
Dynamic Parameters						
Total Gate Charge	Q _{G(-4.5V)}	V _{DS} =-20V, V _{GS} =-4.5V I _D =-4.0A	-	6.8	-	nC
Gate-Source Charge	Q _{GS}		-	3.0	-	
Gate-Drain Charge	Q _{GD}		-	3.3	-	
Input Capacitance	C _{ISS}	V _{DS} =-12V, V _{GS} =0V f=1MHz	-	681	-	pF
Output Capacitance	C _{OSS}		-	290	-	
Reverse Transfer Capacitance	C _{RSS}		-	112	-	
Turn-On Time	t _{D(ON)}	V _{DD} =-12V, I _D =-4A V _{GEN} =-10V, R _G =3.3Ω	-	10	-	nS
	t _R		-	16	-	
Turn-Off Time	t _{D(OFF)}		-	24	-	
	t _F		-	22	-	

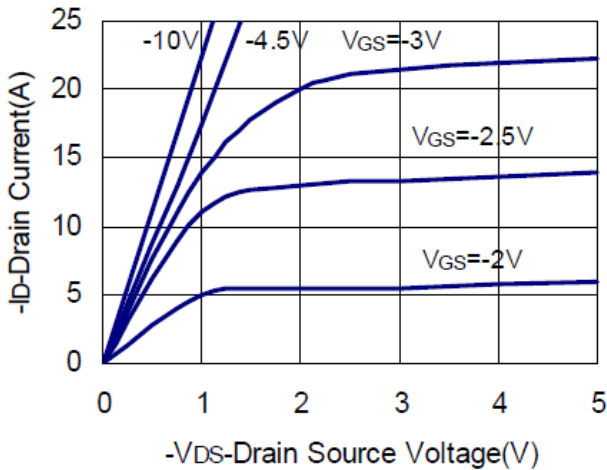
NOTE3: The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



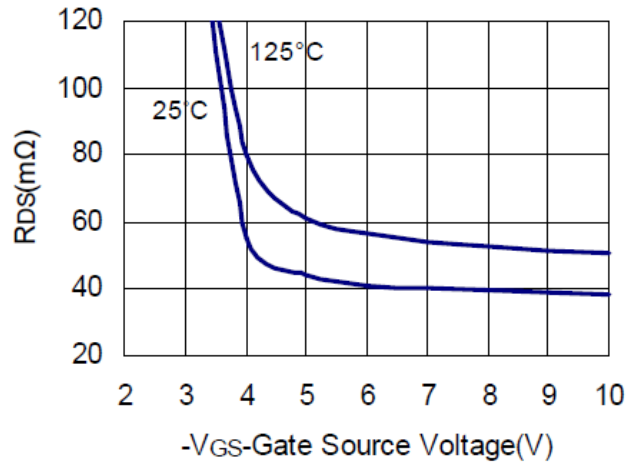
TYPICAL CHARACTERISTICS

25°C, unless noted

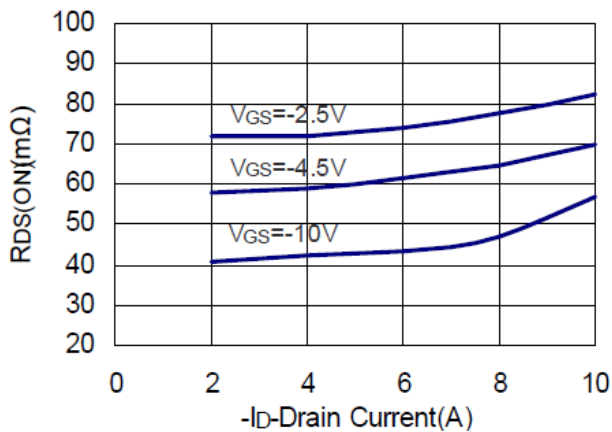
1. Output Characteristics



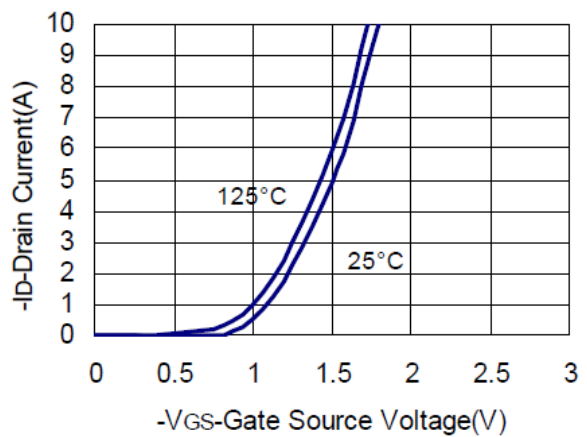
2. Drain-Source On Resistance



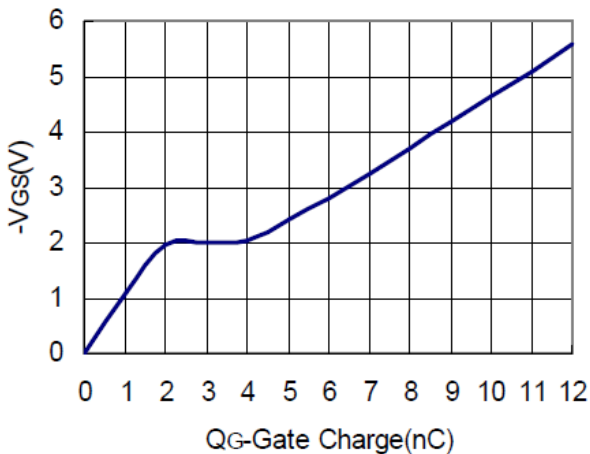
3. Drain Source On Resistance



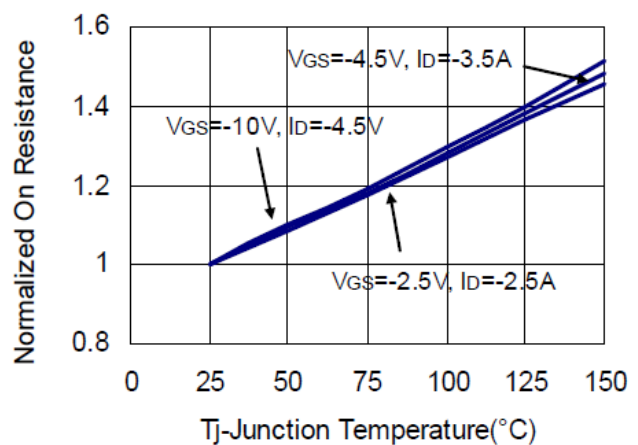
4. Transfer Characteristics



5. Gate Charge

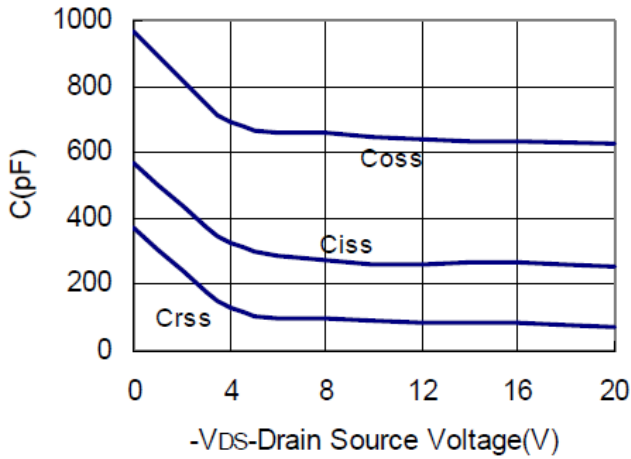


6. Drain Source Resistance

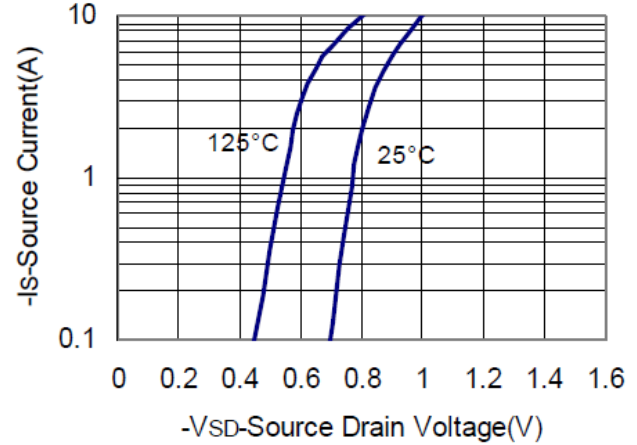




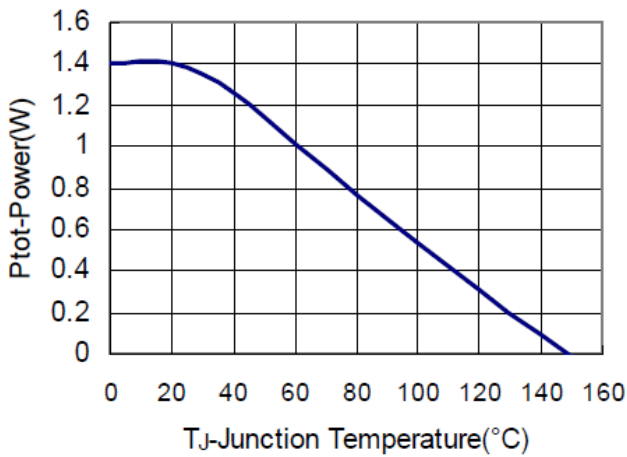
7. Capacitance



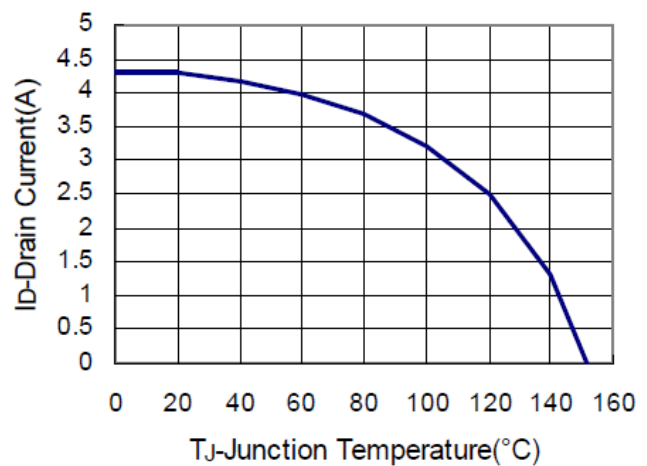
8. Source Drain Diode Forward



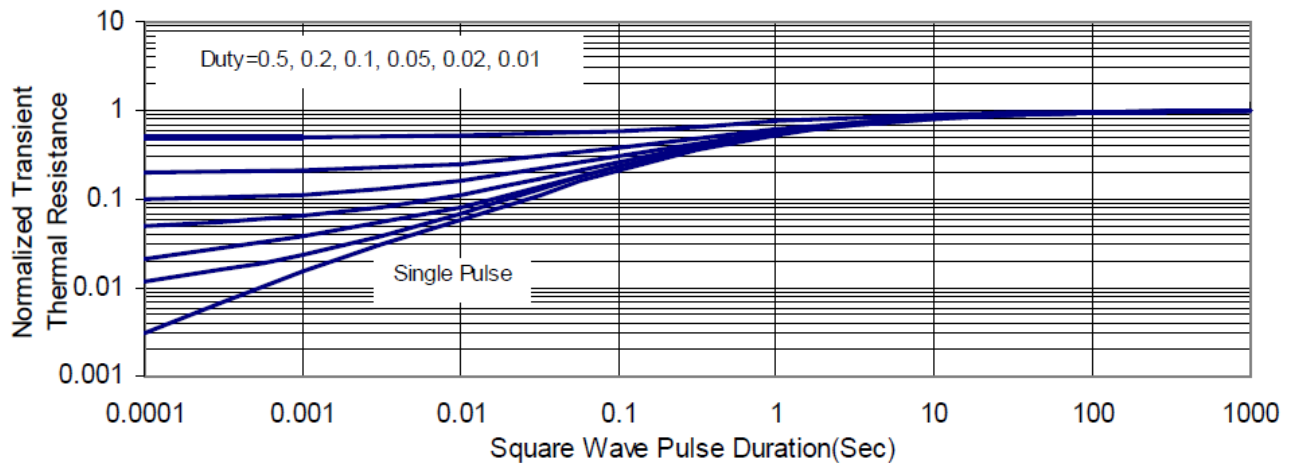
9. Power Dissipation



10. Drain Current



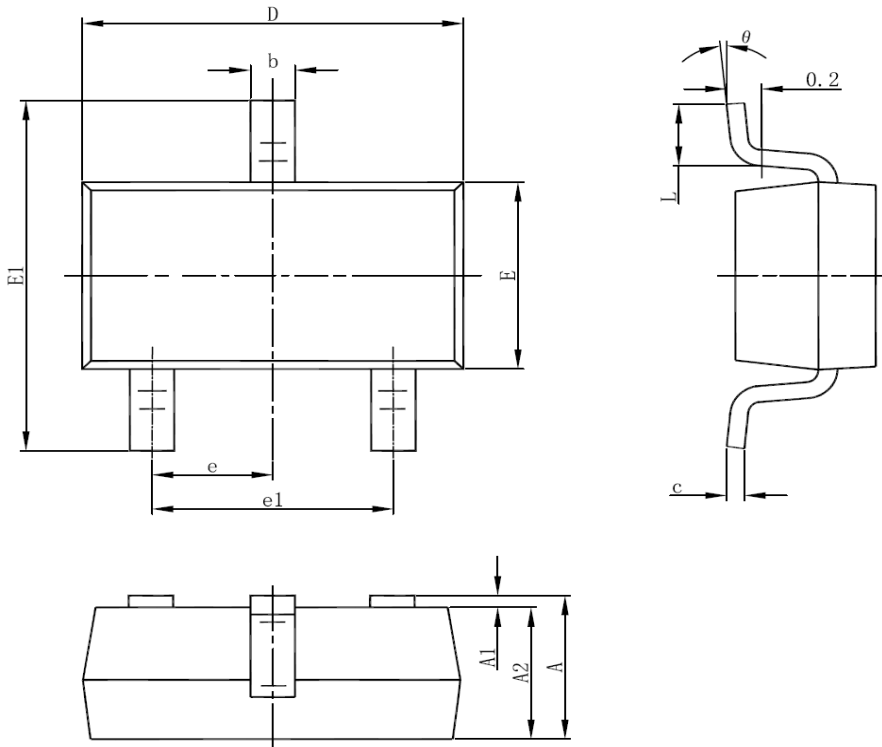
11. Thermal Transient Impedance





PACKAGE INFORMATION

Dimension in SOT-23 Package (Unit: mm)



SYMBOL	MIN	MAX
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950(TYP.)	
e1	1.800	2.000
L	0.300	0.500
θ	0°	8°



IMPORTANT NOTICE

AiT Semiconductor Inc. (AiT) reserves the right to make changes to any its product, specifications, to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

AiT Semiconductor Inc.'s integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life support applications, devices or systems or other critical applications. Use of AiT products in such applications is understood to be fully at the risk of the customer. As used herein may involve potential risks of death, personal injury, or severe property, or environmental damage. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

AiT Semiconductor Inc. assumes to no liability to customer product design or application support. AiT warrants the performance of its products of the specifications applicable at the time of sale.